

AN-XA7XUXXX-X Series

Rev. A

HF SMD TCXO/VCTCXO Ultra-Low Phase Noise With Low G Sensitivity

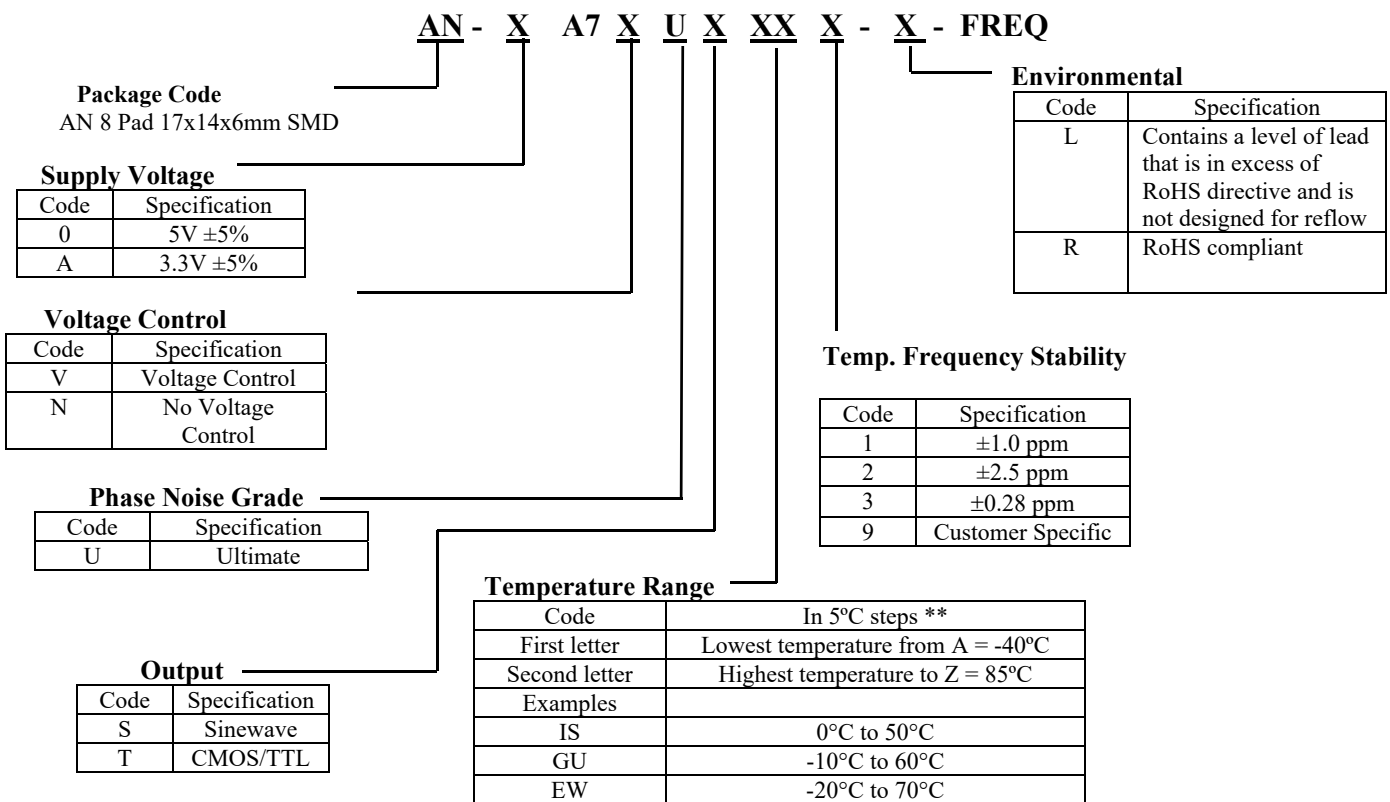
Description: The AN-XA7UXXXX Series of SMD temperature compensated crystal oscillators (TCXO), provides High Frequency with excellent temperature stability, extremely low phase noise and jitter with Sine-wave or CMOS output in a small surface mount FR4 based package.

Features

- Small, Low Profile SMD Package
- Ultra-Low Phase Noise
- Excellent Frequency Stability
- Low G sensitivity
- No Multiplication – no sub-harmonics
- Stratum3 available
- COTS/Dual use



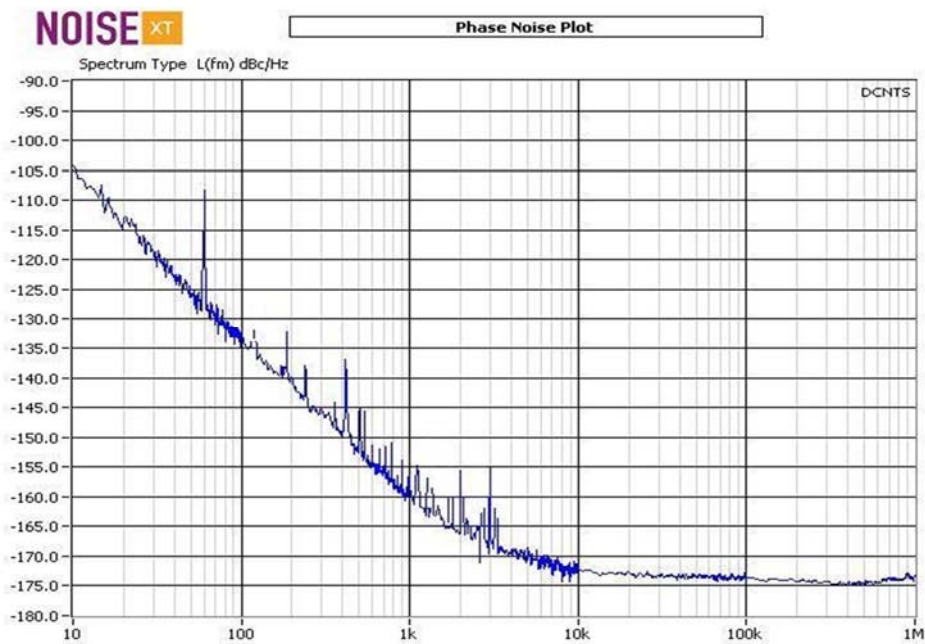
Creating a Part Number



****Temperature Code Table**

Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C
A	-40	F	-15	K	10	P	35	U	60	Z	85
B	-35	G	-10	L	15	Q	40	V	65		
C	-30	H	-5	M	20	R	45	W	70		
D	-25	I	0	N	25	S	50	X	75		
E	-20	J	5	O	30	T	55	Y	80		

*Note: Not all combinations are available



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Note 1) All parameters, unless otherwise specified, are at nominal conditions, ie: T=25°C, Nominal Vcc & Nominal Load

Specifications

Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
Electrical							
Frequency Range	F	Sine-wave	60		125	MHz	
Input Voltage	Vcc		3.135 4.75	3.30 5.0	3.465 5.25	V	A 0
Input Current	Icc	Sine			40	mA	@100MHz, 3.3V
Frequency Stab.	ΔF/F	Overall, available			±4.6		20 years
Frequency Stability	ΔF/F	vs. Temperature vs. Vcc aging		±0.5 ±0.1 ±1 ±2.5 ±3.5	±1	ppm ppm/V ppm/year ppm ppm	See chart First Year 7 years 10 years
G-sensitivity		Gamma		0.2		ppb/G	Preliminary
Calibration	ΔF/F	As shipped, 25°C		±0.5	±1	ppm	
Load		Sine	Internally AC-coupled 50 Ohm				
Output power (output code "S")	P	Sine-wave Into 50 Ohms	7 10	10 13		dBm	3.3V 5.0V
Logic 1 (CMOS)	Voh		0.7Vcc			V	Output Code T
Logic 0 (CMOS)	Vol				0.1V	V	Output Code T
Duty Cycle			45/55		55/45	%	Output Code T
Rise/Fall Time	Tr/Tf			2	3		Output Code T
Spurious					-60	dBc	
Harmonics		Sine-wave		-30	-25	dBc	
SSB		@10Hz @100 Hz @1 KHz @10 KHz @100 KHz		-95 -125 -155 -165 -170			@100MHz, Grade U
Input Impedance			>10K Ohm				
Control voltage	Vc		0		3.0	V	
Modulation bandwidth	MB				1.5	Hz	
Deviation	ΔF/F	Vc=0V to 3.3V,25°C	±5	±7		ppm	

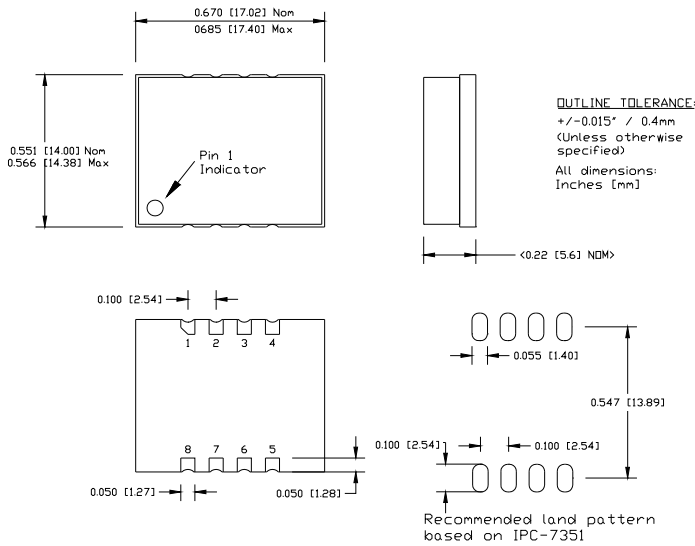
Absolute Maximum Ratings

Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
Input Break Down Voltage	Vcc		-0.5		5.5	V	
Storage temp.	Ts		-40		105	°C	
Contr. Voltage	Vc		-1		4	V	

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Environmental and Mechanical

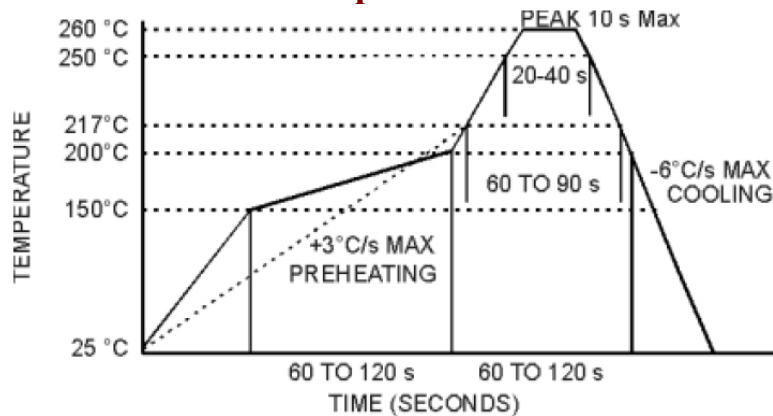
Operating temp. range	-20°C to 70°C MAX, for wider range contact factory, see table to specify.
Mechanical Shock	Per MIL-STD-202, Method 213, Cond. E
Thermal Shock	Per MIL-STD-883, Method 1011, Cond. A
Vibration	Per MIL-STD-883, Method 2007, Cond. A
Soldering Conditions	See MAX reflow profile; The device may be reflowed once. Reflowing upside down is not allowed. NO CLEAN assembly is recommended.
Hermetic Seal	Leak rate less than 1×10^{-8} atm.cc/s of helium (crystal only)



Electrical Connections

Pin out	Pin 1=Vcc; Pin 2, Pin 3, Pin 4, Pin 8 = GND; Pin 5=Output; Pin 6= Optional Voltage Control; Pin 7 Do Not Connect
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Maximum solder reflow profile



The device may be reflowed once. Reflowing upside down is not allowed. NO CLEAN assembly is recommended.