

# O-CS22-HH-0XYZXX-X-X-X-X Series

## Precision Ultra Low Phase Noise OCXO in Low Profile 22x25x10 mm SMD package

### Product Data Sheet

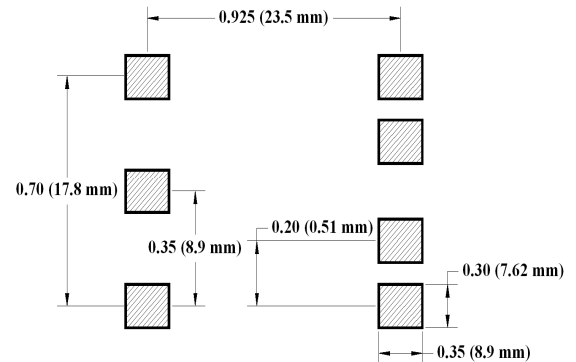
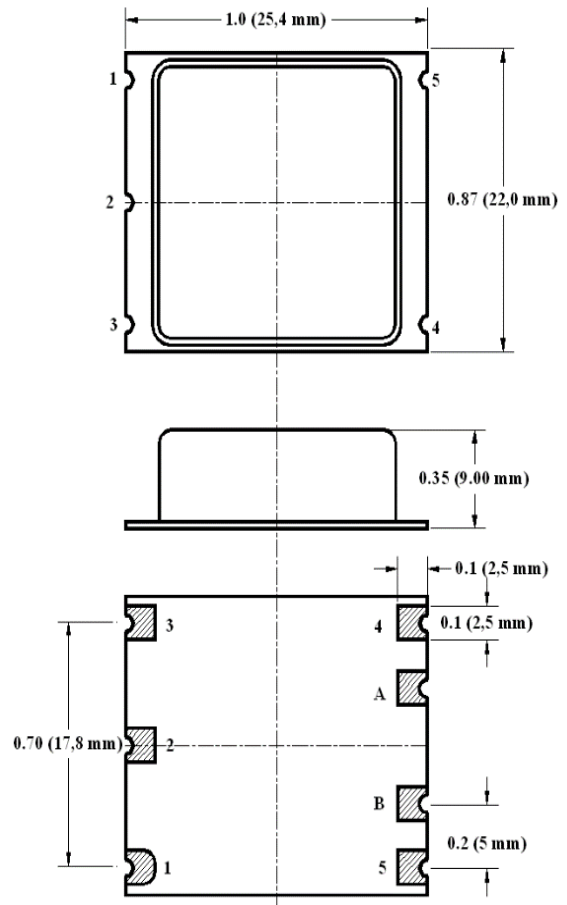
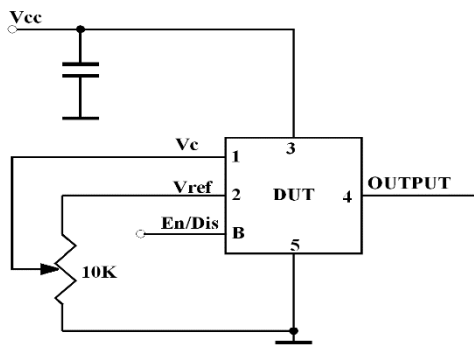
#### Features

- SC-cut crystal
- High Stability
- Low Profile (10 mm) SMD package
- Low Aging
- Ultra Low Phase Noise Option:  
 Premium(P) -145dBc/Hz at 10Hz;  
                   -172dBc/Hz on the floor  
 Ultimate(U) -115 dBc/Hz at 1 Hz  
                   -146dBc/Hz at 10Hz;  
                   -172dBc/Hz on the floor  
 Extraordinary(E) -120 dBc/Hz at 1 Hz  
                   -148 dBc/Hz at 10 Hz  
                   -172 dBc/Hz on the floor
- Sine Wave or HCMOS/TTL output

#### Applications

- Instrumentation
- Tele/Data Communications
- GPS

Height, H	Code
0.350"(9.0 mm)	09
0.394"(10.0 mm)	10



Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
<i>Absolute Maximum Ratings</i>							
Input Break Down Voltage	Vcc	5 V supply	-0.5		5.5	V	
Storage temper.	Ts		-50		90	°C	
Control Voltage	Vc		-1 -1		5.5 11	V	Slope option "P" Slope option "L"

**Electrical**

Frequency	F		8	10.000	13	MHz		All parameters for 10 MHz
Frequency stability	ΔF/F	vs. Temp.		20		ppb	See chart below	
		vs. Supply		0.2	0.3	ppb/10%Vcc		
Aging		per day per year, first year second year		5E-10 5E-8 3E-8			after 30 days of continuous operation	
Allan Deviation		0.1s 1s 10s		5E-13 2E-12 5E-12			Premium version, Option "P"	
SSB Phase Noise (achieved after 10 minutes warm-up)	Sp	1Hz 10 Hz 100 Hz 1 KHz 10 KHz 100 KHz			-112 -145 -155 -162 -169 -172	dBc/Hz	Premium version, option "P"	
		1Hz 10 Hz 100 Hz 1 KHz 10 KHz 100 KHz		-115	-114 -146 -156 -163 -169 -172		Ultimate version, option "U" 2*	
		1Hz 10 Hz 100 Hz 1 KHz 10 KHz 100 KHz			-120 -148 -160 -168 -170 -172		Extraordinary version, option E, available with slope option L	
Retrace		After 30 minutes			±10	ppb	24 Hours off 3*	
G-sensitivity		worst direction			±1.0	ppb/G		
Input Voltage	Vcc		4.75	5.0	5.25	V		
Power consumption, Still air 4*	P	steady state, 25°C, start-up @ -30°C		0.6 2.0	0.8 2.5	W	Operating temp range to 70°C	
Spectral Purity		Subharmonics Spurious Harmonics		none -35	-80 -30	dBc		
Load		10KOhm//15pF (HCMOS/TTL), AC-coupled 50 Ohm (Sine-wave)					Output Code T Output Code S	
Warm-up time	τ	to 0.1ppm accuracy		3	5	minutes		
Output Waveform		HCMOS/TTL compatible or Sinewave						
Output Power			+10	+13		dBm	Output Code S	
Logic 1 (CMOS)	Voh		3.3			V	Output Code T	
Logic 0 (CMOS)	Vol				0.1	V	Output Code T	
Control voltage	Vc		0 0		4.5 10.0	V	Slope option "P" Slope option "L"	
Output Enable		CMOS Logic "1" (4.5V>V>2.5) or floating Logic "0" (V<0.5V)	Enabled Disabled	V		Pout< -30 dBm	Optional	



<b>Input impedance</b>	Zin	At Vc pin	10			KOhm	
<b>Modulation bandwidth</b>	Fm				1,000	Hz	
<b>Reference Voltage</b>	Vref			4.5		V	
<b>Output Impedance</b>		At Vref pin		100		Ohm	
<b>Pull range</b>		from nominal F	±0.4	±0.6		ppm	
<b>Deviation slope</b>		Monotonic, positive Monotonic, positive		1.0/Vref 0.12		ppm/V	Slope option "P" Slope option "L"
<b>Setability</b>	Vc0	@25°C, Fnom. Internal bias is optional, specify on PO 2.25 V for "P", 4.5 V for "L"	2.25 ± 0.5 5 ± 0.5			V	Slope option "P" 3* Slope option "L"

**Notes:**

\*. For highest operating temperature higher than 70°C the power consumption will be higher (about 20% for 85°C). Values listed are for test in still air environment, the values will go up while testing in the temperature chamber.

2\*. This specification is preliminary. It is recommended to specify Slope option "L" for Ultimate Phase noise performance. Recommended test equipment – Symmetricom 5120A-01 Phase Noise and Allan Deviation Test Set (be aware of limitations on the floor, especially if the DUT frequency is not 10.000 MHz), Noise XT DCNTS, or Holtzworth HA7000B series.

"Clean" analog power supply i.e., HP E3610A or equivalent. It's assumed that phase noise test is performed under static conditions (no vibration), in still air, and care is taken for minimizing EMI.

3\*. Longer storage time, especially at low temperatures, may affect both retrace and setability parameters. It may require few days on power for re-stabilization.

4\*. The power consumption is affected by the operating temperature range (the higher the highest temperature – the higher the power consumption. The values in the table are for high operating temperature at 70°C.

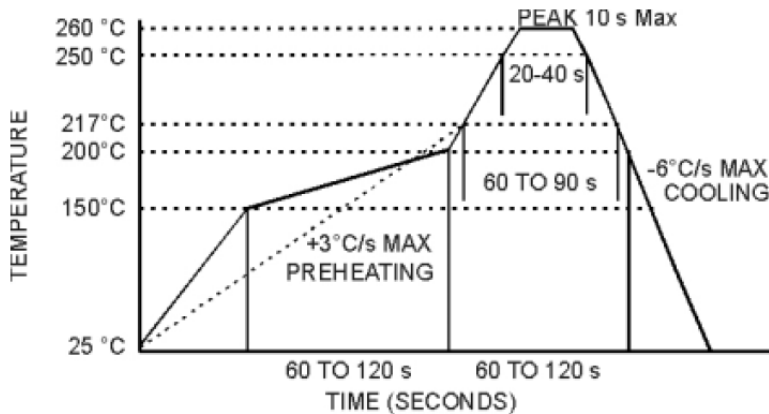
**Environmental and Mechanical**

<b>Operating temp. range</b>	0°C to 70°C Standard, Other options – see chart below
<b>Mechanical Shock</b>	Per MIL-STD-202, 30G, 11ms
<b>Vibration</b>	Per MIL-STD-202, 5G to 2000 Hz
<b>Soldering Conditions</b>	See profile below. The device may be reflowed once. Reflowing upside down is not allowed. Hand soldering is highly encouraged. NO CLEAN assembly is recommended

**Electrical Connections**

<b>Pin Out</b>	Pad #1-Vc; Pad#2 – Vref; Pad #3 – Vcc; Pad #4- Output; Pad #5- GND; Pad A – N/C; Pad B – Output Enable
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**MAX Reflow Profile**



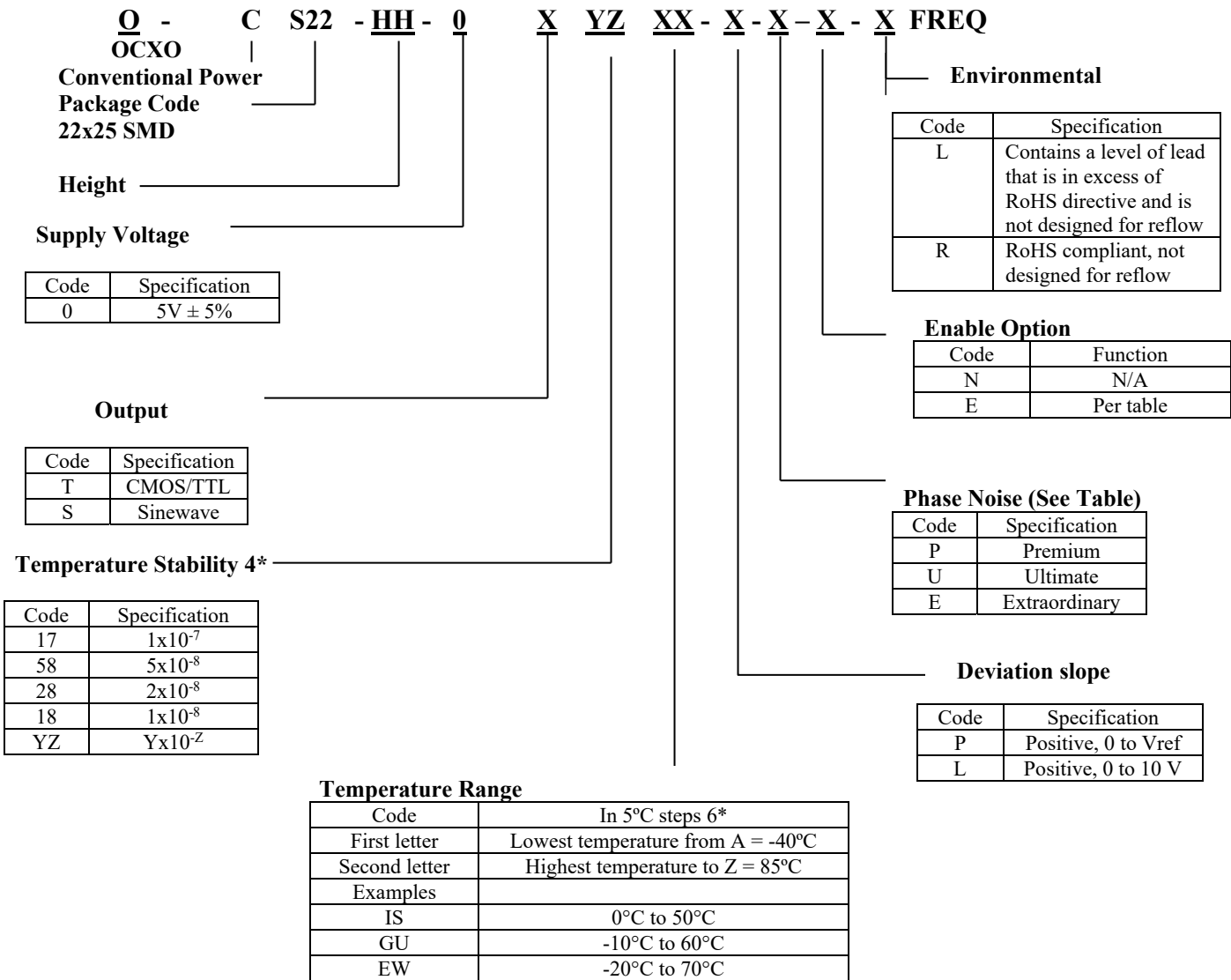
**Creating a Part Number**



**FREQUENCY CONTROLS, INC.**

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Not all combinations are available. Consult Factory.

**6\*Temperature Code Table**

Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C
A	-40	F	-15	K	10	P	35	U	60	Z	85
B	-35	G	-10	L	15	Q	40	V	65		
C	-30	H	-5	M	20	R	45	W	70		
D	-25	I	0	N	25	S	50	X	75		
E	-20	J	5	O	30	T	55	Y	80		

